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SEP 2 8 2004 🔛	ATTY. DOCKET NO. SP036.C10 (1397.012000A)	APPLICATION NO. 10/713,145		
FORM PTO-1449	FIRST NAMED INVENTOR: BRASHEARS et al.			
Third Supplemental	FILING DATE	ART UNIT		
. INFORMATION DISCLOSURE STATEMENT	November 17, 2003	2183		

U.S. PATENT DOCUMENTS								
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INITIAL	AA14	NUMBE	:R	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AB14							
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	AD44	F 276 6	040	01/1004	Callaghar at al			
\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	AD14	5,276,848		01/1994	Gallagher et al.			
W	AE14	4,901,	233	02/1990	Liptay			
4	AF14	5,134,	561	07/1992	Liptay			
6	AG14	5,619,	730	04/1997	Ando			
5	AH14	5,930,	520	07/1999	Ando			
2	Al14	4,679,	141	07/1987	Pomerene et al.			
~	AJ14	5,418,9	973	05/1995	Ellis et al.			
	AK14							
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10	AL11	64-363		02/07/1989	Japan	05.100		Ys
	AM11	64-86243		03/30/1989	Japan			X No X Y s No
W	AN11	4-1537	'33	05/27/1992	Japan			Yes X No
(V)	AO11	2-4873	2	02/19/1990	Japan			X Yes
W	AP11	63-192	:135	08/09/1988	Japan			Yes X No
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N	AL12	0 272 198	06/22/1988	EPO			Yes No
	AM12						Yes No
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0		=	Tetsuya HARA et al., "Organizations of An Extended Superscalar Processor Prototype Based on the SIMP (Single Instruction Stream/Multiple Instruction Pipelining) Architecture", Electronic Information Communication Society Technical Research Report CPSY90-38-63, Electronic Information Communication Society, Inc., July 20, 1990, Vol. 90, No. 144, pp 103-108 (with English language translation, 14 pages).						
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